# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 0 | ((programmable adj logic) and (switch adj element) and (control adj circuit) and (configuration adj memory) and (configuration and data) and (design adj time)).clm. | US-PGPUB | OR | OFF | 2006/02/08 14:41 |
| L2 | 0 | ((programmable adj logic) and (voltage adj regulator) and (control adj circuit) and (configuration adj memory) and (configuration and data) and (design adj time)).clm. | US-PGPUB | OR | OFF | 2006/02/08 14:42 |
| L3 | 0 | ((programmable adj logic) and (voltage adj regulator) and (control adj circuit) and (configuration adj memory) and (configuration and data)).clm. | US-PGPUB | OR | OFF | 2006/02/08 14:41 |
| L4 | 0 | ((programmable adj logic) and (switch adj element) and (control adj circuit) and (configuration adj memory) and (configuration and data)).clm. | US-PGPUB | OR | OFF | 2006/02/08 14:42 |
| L5 | 0 | ((programmable adj logic) and (switch adj element) and (control adj circuit) and (configuration adj memory)).clm. | US-PGPUB | OR | OFF | 2006/02/08 14:42 |
| L6 | 0 | ((programmable adj logic) and (voltage adj regulator) and (control adj circuit) and (configuration adj memory)).clm. | US-PGPUB | OR | OFF | 2006/02/08 14:42 |